

Power-on script

- ☐ Soft-start is clean; no HS/LS shoot-through
- ☐ UVLO/PS sequence correct; PWM idle state OK
- ☐ No unstable switching at light load

Waveforms

- ☐ HS/LS gate timing verified; proper dead-time
- ☐ SW node ringing within limit; add snubber if needed
- ☐ Boot voltage OK; no droop on transients

telemetry & limits

- ☐ IMON scale/offset calibrated at 3 points
- ☐ OCP type (latched/cycle) verified and trip level
- ☐ OTP behavior verified; case vs reported temp

Thermal

- ☐ Case temp at I_nom within budget
- ☐ IR hot-spot map matches layout intent
- ☐ Airflow direction quantified

Efficiency grid

- ☐ Matrix Fsw x Iout (3x3) measured
- ☐ A/B candidates compared at same conditions
- ☐ Store CSV and thermal images

Sign-off

- ☐ Reviewer:
- ☐ Date:
- ☐ Version: