

Layout & EMI Notes (Charger)

- Minimize SW loop; place input cap close to high-side switch; use solid GND.
- Kelvin sense BAT/NTC; keep sense away from SW; star ground analog returns.
- Thermal: vias under pad; spread heat to inner planes; respect keep-outs.
- EMI: snubber RC @SW as needed; filter near connector; slow edges within spec.
- Validation: LISN scan, radiated pre-scan, hot-box test with logging.